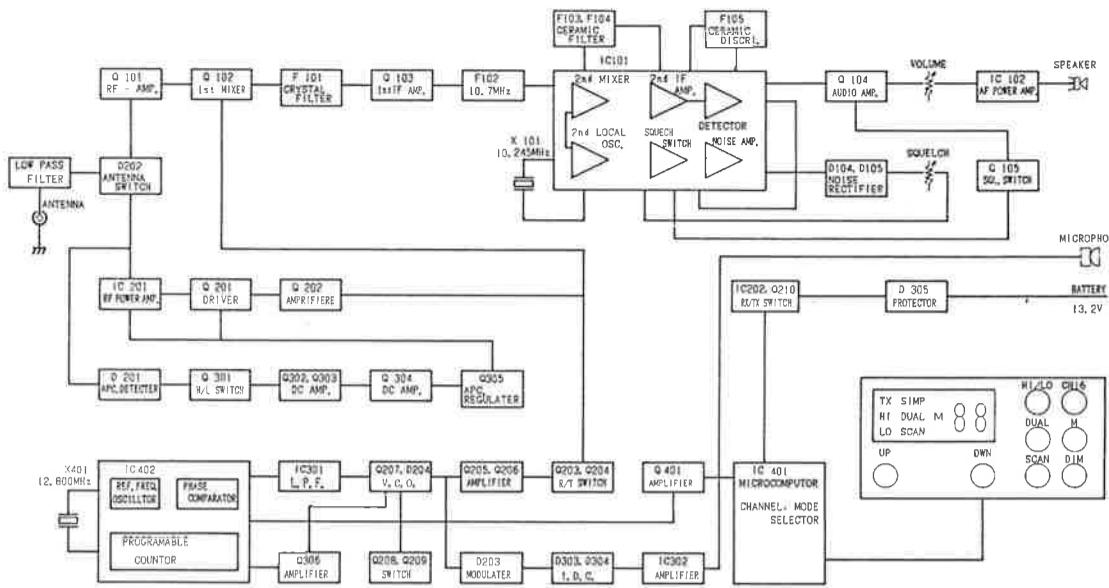


BLOCK DIAGRAM



CIRCUIT OPERATIONS

Phase Locked Loop (P.L.L.) and Voltage Controlled Oscillator (V.C.O.)

The oscillating frequency of V.C.O. (Q207) is set as (channel frequency - 10.7) MHz, in receiving and (channel frequency) MHz, in transmitting, and the channel spacing is 25 KHz. Press the channel selector switch and select the channel number you want, which will be indicated on the LCD display. The selected channel is entered to the microcomputer (IC401). The microcomputer conveys the counter number corresponds to the registered channel number to the programmable counter (IC402).

The counter number to be conveyed to the programmable counter will be transmitted with the synchronized serial data system. The counter number entered to the programmable counter will be latched until the next number is entered. The counter number will be calculated with the following methods.

$$\text{When receiving } N = \frac{\text{Channel Frequency (MHz.)}}{0.00625} - 10.7$$

$$\text{When transmitting } N = \frac{\text{Channel Frequency (MHz.)}}{0.00625} + 10.7$$

The oscillating frequency of V.C.O. is amplified at AMP (Q306) and entered to the programmable counter. The programmable counter divides the V.C.O. oscillating frequency with the "N" value which is the counter number. The oscillating circuit inside of IC402 will do a crystal oscillation of 12.8 MHz. The crystal oscillating frequency 12.8 MHz, will be divided and becomes to the reference frequency of P.L.L.

The P.L.L. compares the oscillating frequency of V.C.O. divided at the programmable counter and the reference frequency, and controls the oscillating frequency in stability through a lowpass filter (IC301) and a voltage controlled capacitor diode (D204).

The oscillating frequency of V.C.O. is amplified at AMP (Q206, Q207) and to be applied to the receiving section (block) or the transmitting section (block).

1. TRANSMITTER (TX)

The channel frequency signal is amplified by amplifiers Q201 & Q202 and drives the RF-power module IC201. In the RF-power module, the channel signal is subject to automatic power control, and amplified to 25W in the high power mode and to 1W in the low power mode. Thus amplified power output is finally fed to the antenna connector in passing through an antenna filter. An antenna filter are provided to reject spurious frequencies adjacent to the channel frequency and harmonic frequencies, respectively.

2. AUTOMATIC POWER CONTROL (A.P.C.)

A signal, which is proportional to the RF-output power of the RF-power module, is detected and rectified by a detector D201. The DC output proportional to the RF-output power is amplified in DC amplifiers Q302, Q303 and Q304, and then applied to the control circuit Q305. The control circuit controls the power supply voltage to the RF power module and the voltage to the driver circuit Q201, thus controlling the antenna output power. Q301 functions as a power output switch which selects either 25W or 1W.

3. MODULATOR

An audio signal developed in a microphone is fed to the audio amplifier IC302 and amplified. The amplitude of the audio signal is limited to the value which gives the maximum deviation by the I.D.C. circuit D303 and D304. The amplified audio signal enters the modulator D203 through a roll-off filter circuit which limits the occupied bandwidth. The modulator uses a voltage controlled capacitor and modulates the V.C.O. frequency with the audio signal.

4. RECEIVER (RX)

Signals induced on an antenna are amplified by the RF amplifier Q101 and all undesirable frequency components outside the band are eliminated in passing through the five stage resonator. The amplified RF signal is mixed with the local frequency signal sent from the V.C.O. in the first mixer circuit Q102, thus developing a first IF signal of 10.7 MHz. The IF signal is then amplified in the first IF amplifier circuits Q103. Undesirable frequency signals including adjacent channel frequencies, etc. caused in conversion process are removed by two stages of crystal filters F101 & F102. The 10.7 MHz. signal amplified in this way is applied to the 2nd mixer circuit and mixed with the second local oscillator frequency of 10.245 MHz. generated in the crystal oscillator circuit Il01 to create a 2nd IF signal of 455 KHz. The 455 KHz. IF signal is amplified in the 2nd IF amplifier circuits IC101. Adjacent interference signals included in the IF signal are also removed with the ceramic filters F103 and F104. Thus processed IF signal is detected with the discriminator F105 and demodulated into the audio signal. The audio signal is amplified by the audio amplifiers Q104 and IC102 and drives the speaker.

5. SQUELCH (SQ)

(In a FM receiver, excessive noise will be heard at no signal condition) A noise amplifier IC101 amplifies noise components in the outputs developed in the discriminator circuit F105 and rectifier diodes D104 and D105 rectify the amplified output to create a DC control signal. The DC control signal turns the electronic switch Q105 on so that the noises can not be amplified. Thus noises will be suppressed at no signal condition. On the other hand, the noises will be reduced when a signal enters through the antenna. Then the DC control signal created from the noise signal also reduces and the electronic switch Q105 turns off, allowing the audio signal to enter the audio amplifier.

6. AUDIO POWER AMPLIFIER

A volume control resistor VR102 adjusts audio signal level and the audio power amplifier IC102 amplifies the adjusted audio signal to a sufficient power level capable of driving the speaker.

7. RX AND TX SWITCHING

Normally, the transceiver is in a RX standby mode. To switch the transceiver to the TX mode, a P.T.T. switch provided on the microphone is pushed. When a transmit operation is available, the microcomputer develops a TX enable signal and this signal is added with the P.T.T. ON signal in the logic circuit IC401 and the resultant output turns on the TX-RX switch IC202 and Q210.

8. SIMPLEX AND DUPLEX OPERATION SWITCHING

Switching operations for the Simplex or Duplex mode is conducted automatically according to a command from the microcomputer.

9. VOLTAGE REGULATOR CIRCUIT

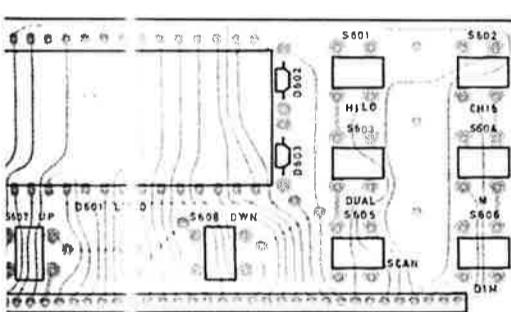
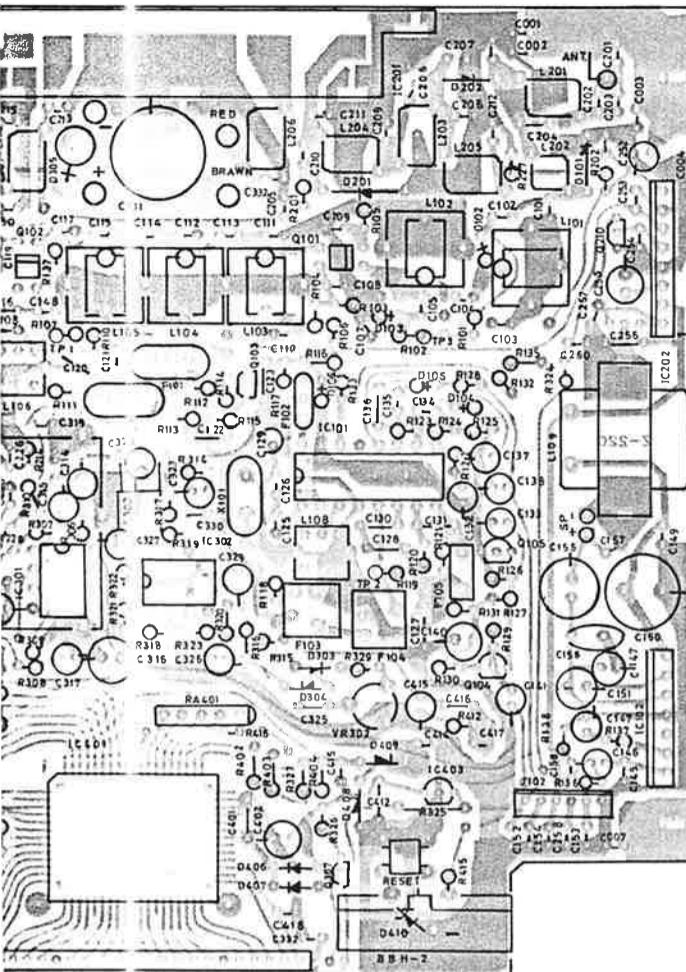
To assure stable operation of the transceiver if the battery voltage varies (10.8V - 15.6V), voltage regulators are provided. IC202 is used to stabilize line voltage for the P.L.L. circuit, receive circuit, and transmit circuit; and IC403 for the microcomputer circuit.

10. DIMMER CIRCUIT

To control brightness of the display on the transceiver a DIMMER switch is provided.

DUAL WATCH OPERATION

When the DUAL WATCH switch is placed in the depressed in position, CH16 and an arbitrary channel selected are received alternately. When the squelch is closed for the CH16 and the selected channel at no signals, receive-standby operation is alternately switched with a ratio of 150 msec for CH16 and 850 msec for the selected channel. When a signal is received through the selected channel, the signal is reproduced for 850 msec, and then the receiver enters the standby operation for 150 msec in the CH16. The receiver repeats the above cycles. When a signal is received through the CH16, the receiver reproduces the signal until the signal disappears. Approx. 700 - 1400 msec after the disappearance, the selected channel is actuated for 850 msec. Then, the receiver repeats the above cycles in the same way.



CIRCUIT ALIGNMENT

ROUTINE:

Connect a frequency counter and an RF-voltmeter to TP-1 (Q102 gate 2).

Connect a DC voltmeter to TP-3 (L215).

Set the CH16 switch to set the CH16.

Just L213 until the DC voltmeter connected to TP-3 indicates 2.0V.

Just L210 until the RF-voltmeter connected at TP-1 indicates a maximum value.

Just VC401 until the frequency counter connected to TP-1 indicates 6.100 MHz.

Set the channel to 16.

Make sure the PLL circuit can lock at a voltage lower than 10V.

ITER:

power adjustment:

1. Connect an RF power meter, frequency counter, and a FM receiver to the antenna connector.

1. Set the channel to 16.

1. Place the 1W - 25W switch in the 1W position.

1. Adjust VR201 : obtain the RF power output of 0.8W.

1. Place the 1W - 25W switch in the 25W position.

1. Adjust VR301 : obtain the RF power output of 25W.

frequency Adjustment:

1. Set the channel to 16.

1. Adjust VC401 to set the antenna frequency to 156.900 MHz.

modulation Adjustment:

1. Place the channel to 16.

1. Feed a 1 KHz, .0mV audio signal to the microphone input circuit.

1. Adjust VR302 : set the FM receiver's deviation to ±4.7 KHz.

1. Adjust the microphone input signal level so that the deviation becomes ±3 KHz.

1. Make sure that distortion of demodulated output of the FM receiver is less than 1%.

frequency Adjustment:

1. Place the channel to 16.

1. Adjust VR302 : set the antenna frequency to 156.800 MHz., deviation of ±3 KHz., the output level of 20 dBm.

the channel to 16.

1. Connect the DC-voltmeter to TP-2 (R119).

1. Just L101, L102, L103, L104, L105 and L106 until the DC-voltmeter connected to TP-2 indicates a maximum value.

the channel to 16.

1. Just the signal generator frequency to 162.00 MHz.

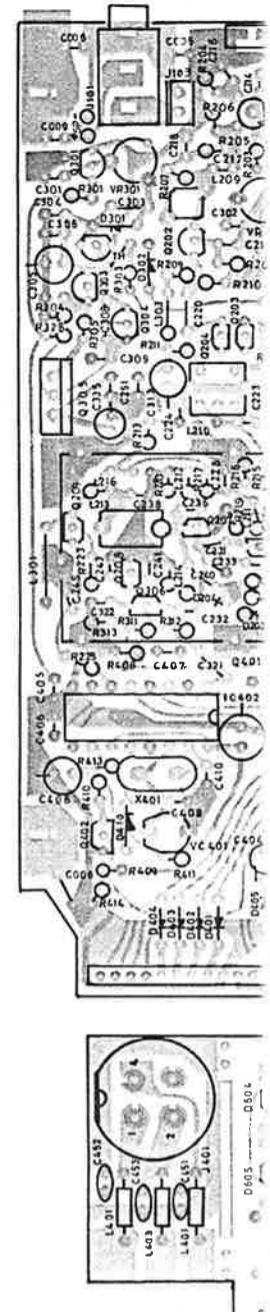
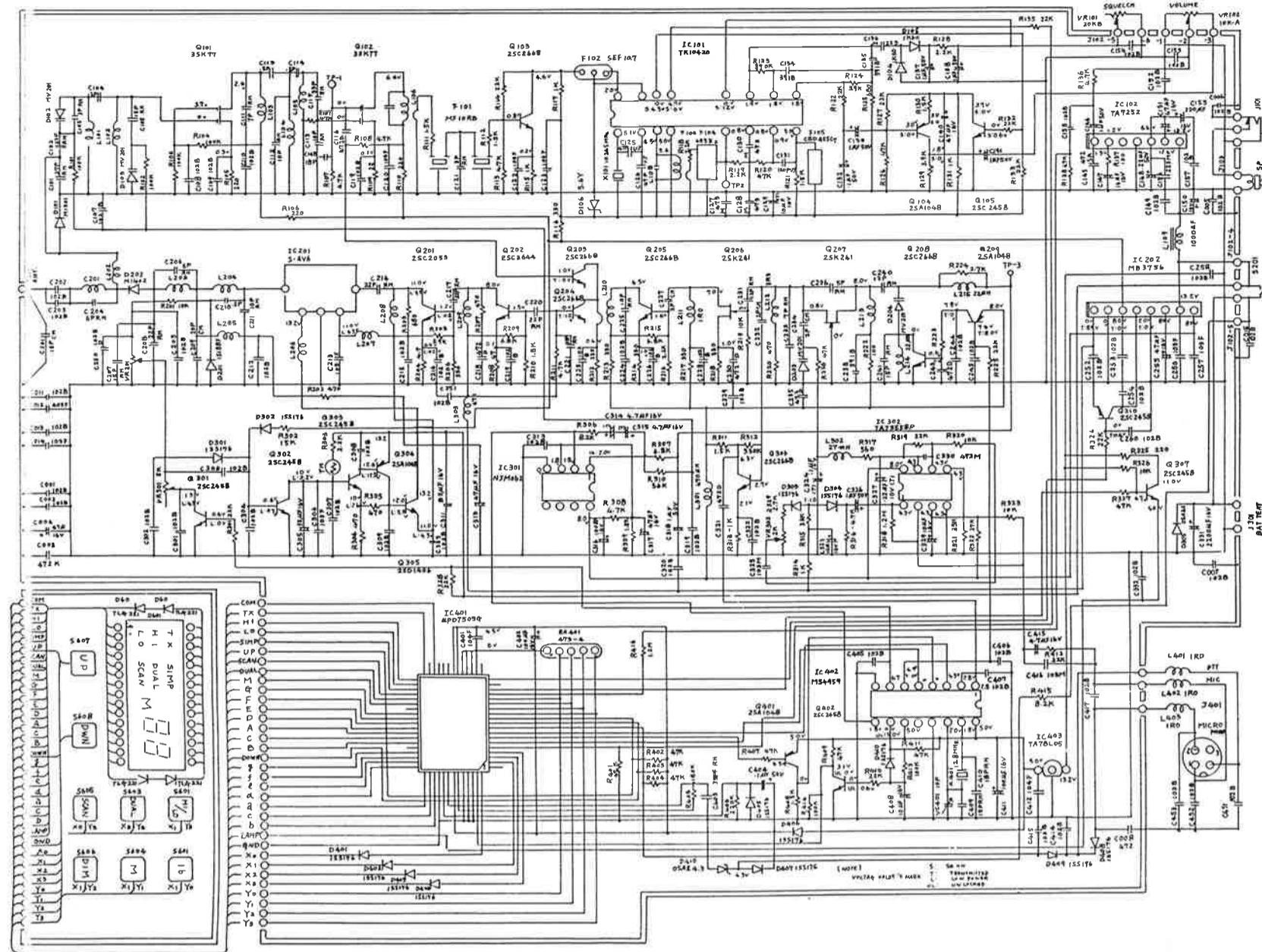
1. Just L104 until the DC-voltmeter connected to TP-2 indicates a maximum value.

make sure 12 dB SIND : sensitivity at channels 06, 16, and 28, and make sure all channels indicate sensitivity of 0.5 uV or higher.

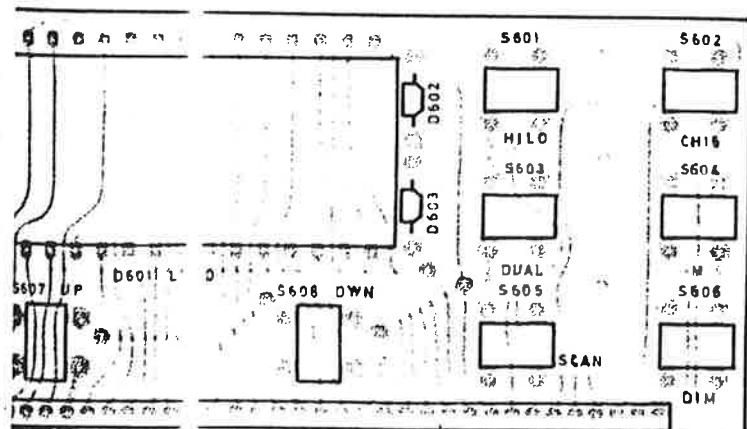
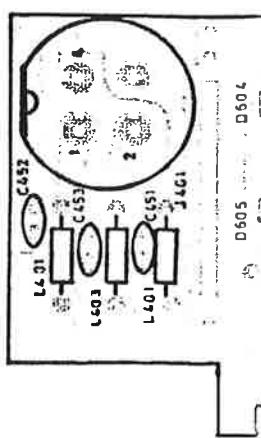
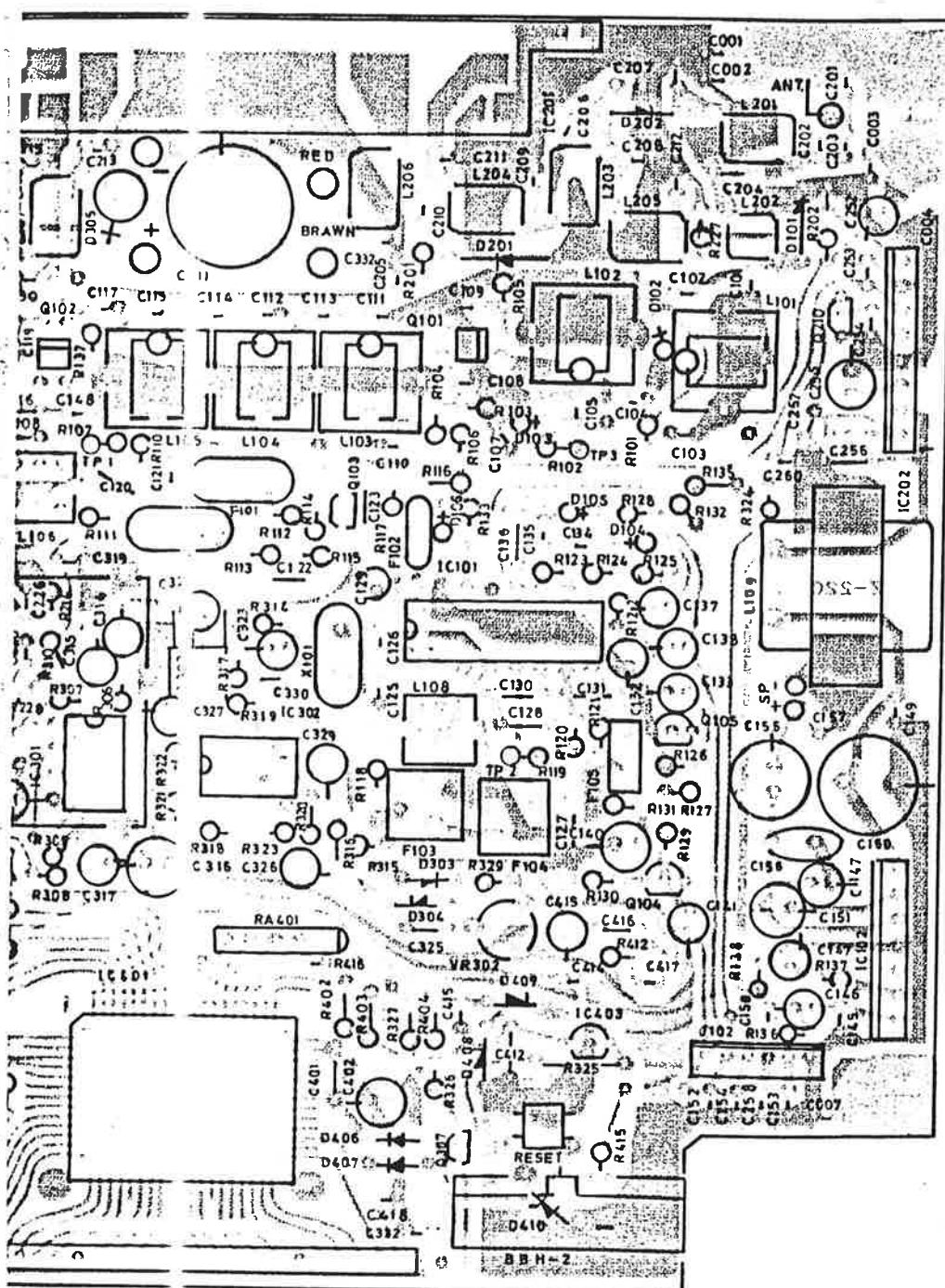
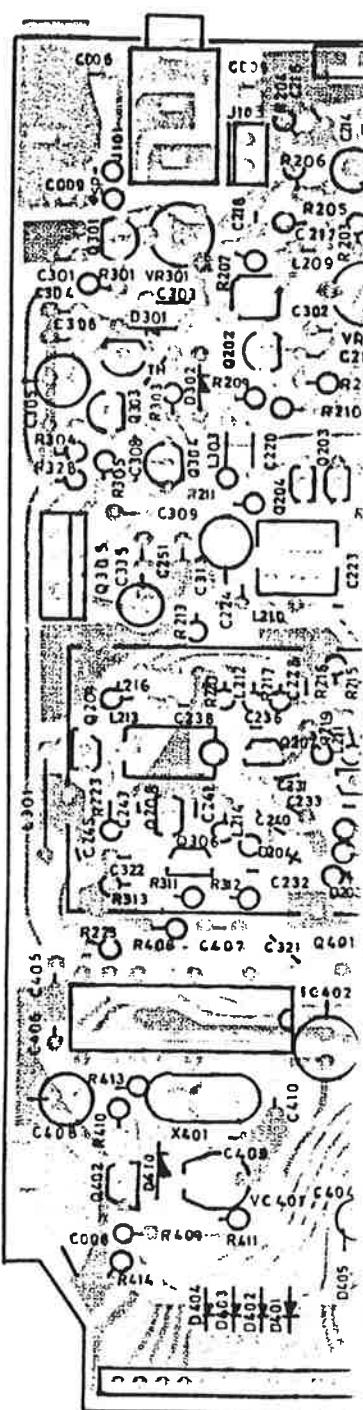
HUSUN COMPACT SERVICE DATA

P.C. BOARD LAYOUT
MAIN LCD

CIRCUIT DIAGRAM



PARTS LIST



CIRCUIT ALIGNMENT

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