

uPD2816 (NEC)

MODELS

AM:

Cobra 29GTL, 29LTD, 29LTD Classic, 63GTL, 87/89/1000GTL

President Dwight D (new), Thomas J, Zachary T (new)

SBE LCB-8

Superstar 120

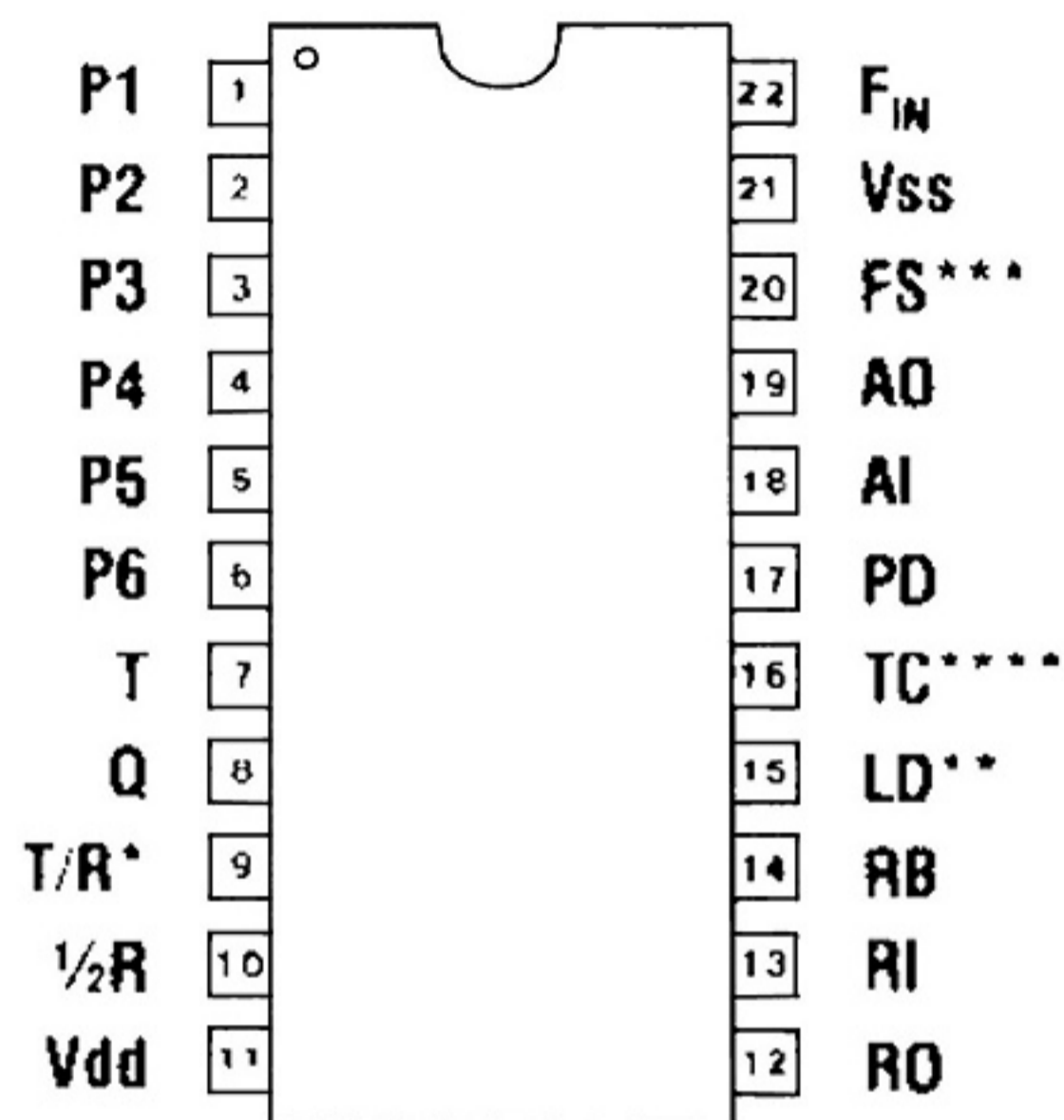
Teaberry Stalker V, XII

(See block mixing diagram.)

SSB:

Midland 6001, 7001 (early)

(See block mixing diagram.)



*R = 1, T = 0

**1 = Locked, 0 = Unlocked

***1 = 10 KHz steps, 0 = 5 KHz steps

****TC is a second unbuffered LD normally just tied HIGH.

Another versatile chip but quickly fading away in AM use for the more secure chips. Uses 6-bit BCD/ROM programming with internal pull-up resistors on the T/R, FS, and programming pins. With FS HIGH, divider uses 10 KHz steps with N-Codes of 91 to 135. With FS LOW, N-Codes are 182 to 270 when T/R is 1, and 273 to 361 when T/R is 0, yielding the standard 455 KHz AM IF offset. See block mixing diagram for AM rigs. For SSB use, T/R pin is not required.

NOTE: An interesting and simple modification for the SSB Midland is to switch the state of the T/R pin; this will give 40 channels beginning 455 KHz below normal Ch. 1.