

LM3189 FM IF System

General Description

The LM3189N is a monolithic integrated circuit that provides all the functions of a comprehensive FM IF system. The block diagram of the LM3189N includes a three stage FM IF amplifier/limiter configuration with level detectors for each stage, a doubly balanced quadrature FM detector and an audio amplifier that features the optional use of a muting (squelch) circuit.

The advanced circuit design of the IF system includes desirable deluxe features such as programmable delayed AGC for the RF tuner, an AFC drive circuit, and an output signal to drive a tuning meter and/or provide stereo switching logic. In addition, internal power supply regulators maintain a nearly constant current drain over the voltage supply range of $\pm 8.5V$ to $\pm 16V.$

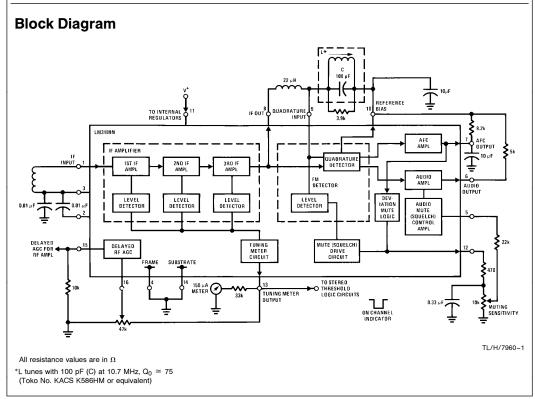
The LM3189N is ideal for high fidelity operation. Distortion in an LM3189N FM IF system is primarily a function of the phase linearity characteristic of the outboard detector coil. The LM3189N has all the features of the LM3089N plus

additions. The LM3189N utilizes the 16-lead dual-in-line plastic pack-

age and can operate over the ambient temperature range of -40° C to $+85^\circ$ C.

Features

- Exceptional limiting sensitivity: 12 µV typ at -3 dB point
- Low distortion: 0.1% typ (with double-tuned coil)
- Single-coil tuning capability
- Improved (S + N)/N ratio
- Externally programmable recovered audio level
- Provides specific signal for control of inter-channel muting (squelch)
- Provides specific signal for direct drive of a tuning meter
- On channel step for search control
- Provides programmable AGC voltage for RF amplifier
- Provides a specific circuit for flexible audio output
- Internal supply voltage regulators
- Externally programmable ON channel step width, and deviation at which muting occurs



© 1995 National Semiconductor Corporation TL/H/7960

RRD-B30M115/Printed in U. S. A.

_M3189 FM IF System

April 1987

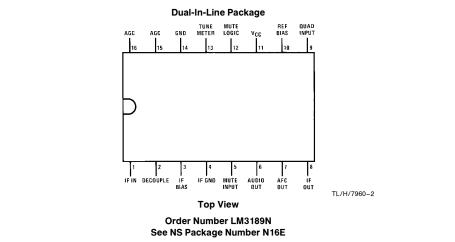
Absolute Maximum Ratings					
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.		Power Dissipation (Note 2)	1500 mW −40°C to +85°C −65°C to +150°C		
		Operating Temperature Range			
		Storage Temperature Range			
Supply Voltage Between Pin 11 and Pins 4, 14	16V	Lead Temperature (Soldering, 10 sec.)	260°C		
DC Current Out of Pin 12	5 mA	Loud Fomporataro (Condoning, To Cool)	200 0		
DC Current Out of Pin 13	5 mA				
DC Current Out of Pin 15	2 mA				

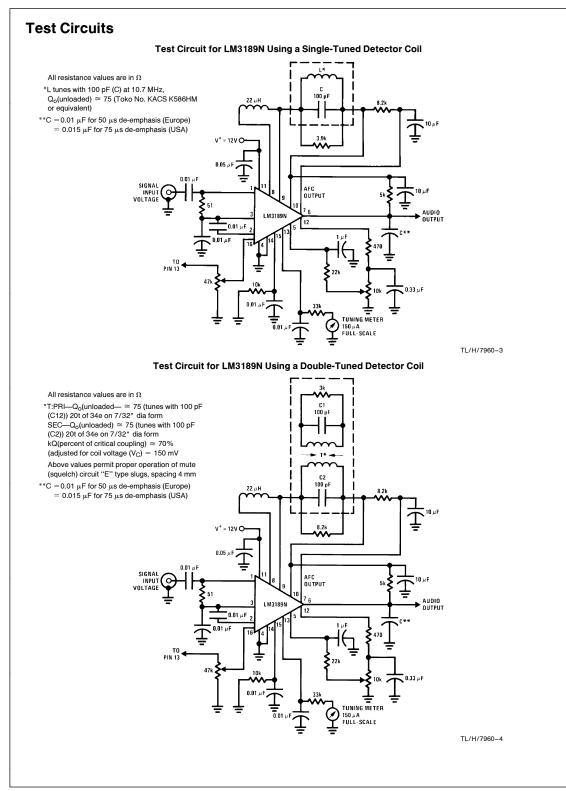
Electrical Characteristics $T_A = 25^{\circ}C$, $V^+ = 12V$

Symbol	Parameter	Con (See Single-Tu	Min	Тур	Max	Units	
STATIC (D	C) CHARACTERISTICS				-		
l ₁₁	Quiescent Circuit Current			20	31	44	mA
V1 V2 V3 V15 V10	DC Voltages: Terminal 1 (IF Input) Terminal 2 (AC Return to Input) Terminal 3 (DC Bias to Input) Terminal 15 (RF AGC) Terminal 10 (DC Reference)	No Signal Input, Nc	1.2 1.2 1.2 7.5 5	2.0 2.0 2.0 9.5 5.75	2.4 2.4 2.4 11 6	V V V V	
DYNAMIC	CHARACTERISTICS						
V _I (lim)	Input Limiting Voltage ($-3 dB$ Point)				12	25	μV
AMR	AM Rejection (Term. 6)	$V_{IN} = 0.1V$	$f_{o} = 10.7 \text{ MHz},$ $f_{mod} = 400 \text{ Hz},$ Deviation $\pm 75 \text{ kHz}$	45	55		dB
V _O (AF)	Recovered AF Voltage (Term. 6)	AM Mod. = 30%		325	500	650	mV
THD	Total Harmonic Distortion (Note 1) Single Tuned (Term. 6) Double Tuned (Term. 6)	$V_{IN} = 0.1V$			0.5 0.1	1	% %
S + N/N	Signal Plus Noise to Noise Ratio (Term. 6)			65	80		dB
f _{DEV}	Deviation Mute Frequency		f _{mod} = 0		±40		kHz
V16	RF AGC Threshold				1.25		V
V12	On Channel Step	$V_{IN} = 0.1V$	$\begin{array}{l} f_{DEV} < \pm 40 \ kHz \\ f_{DEV} > \pm 40 \ kHz \end{array}$		0 5.6		v

Note 1: THD characteristics are essentially a function of the phase characteristics of the network connected between terminals 8, 9, and 10. Note 2: For operation in ambient temperatures above 25°C, the device must be derated based on a 150°C maximum junction temperature and a thermal resistance of 80°C/W junction to ambient.

Connection Diagram

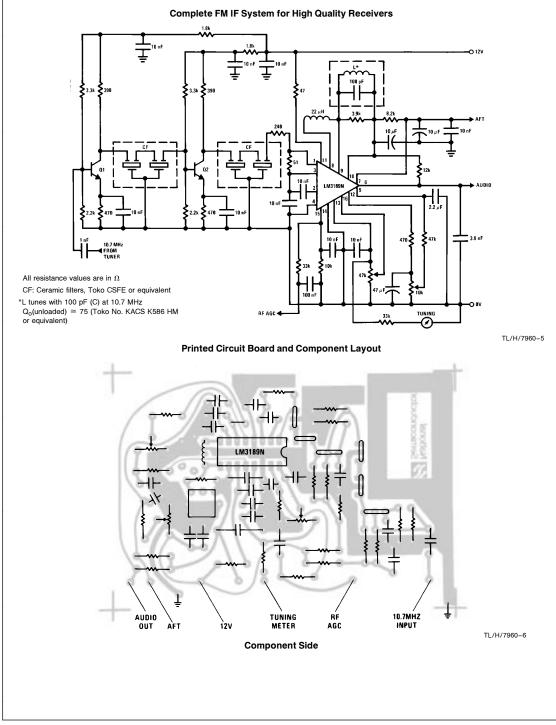


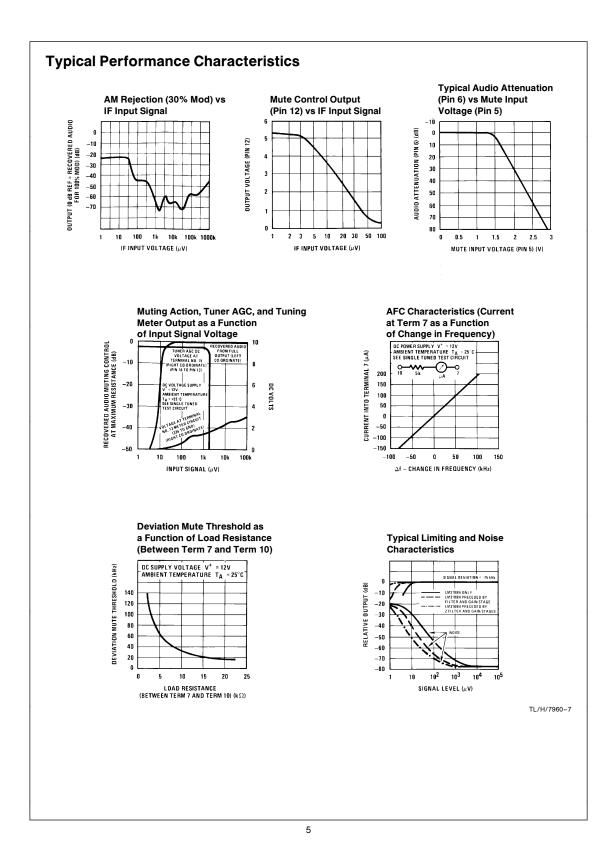


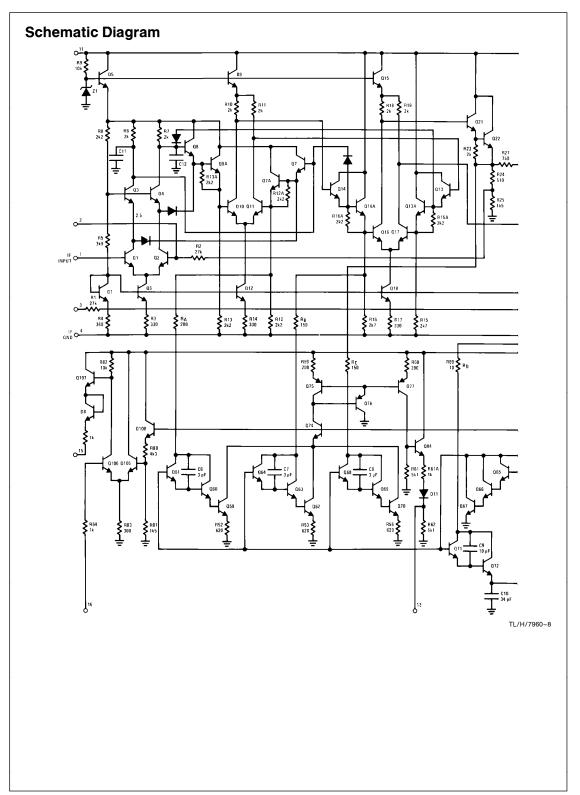


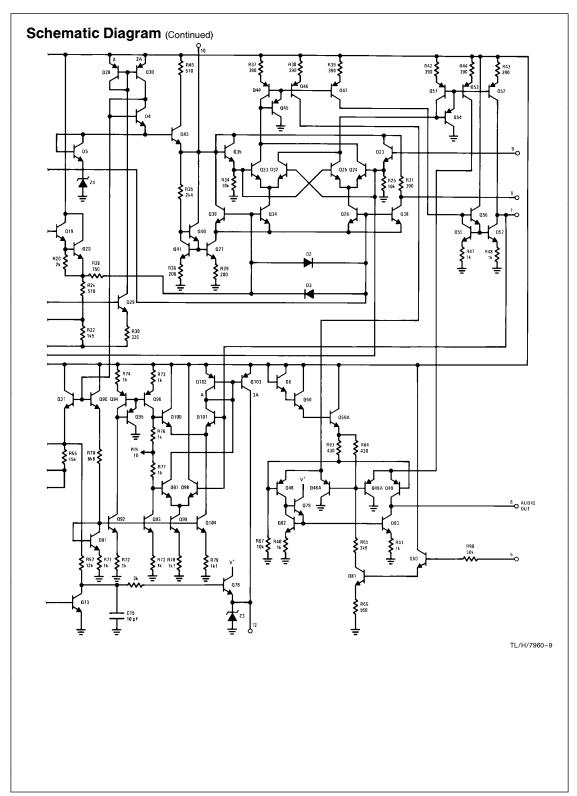
The circuit provides a complete FM IF system for a high quality receiver. Either one or two stages of amplification and bandpass filtering may be desired, depening on the

receiver requirements. See graph for Typical Limiting and Noise Characteristics for each circuit configuration which can be compared to the LM3189N alone.

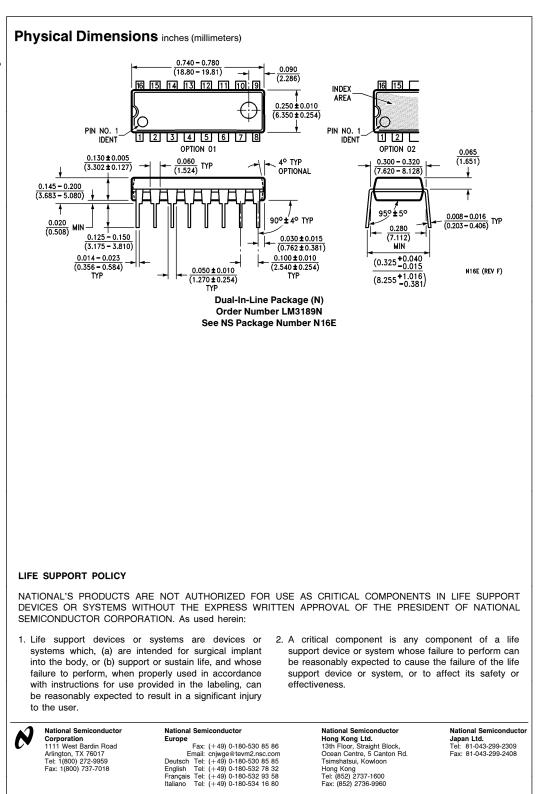












National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications