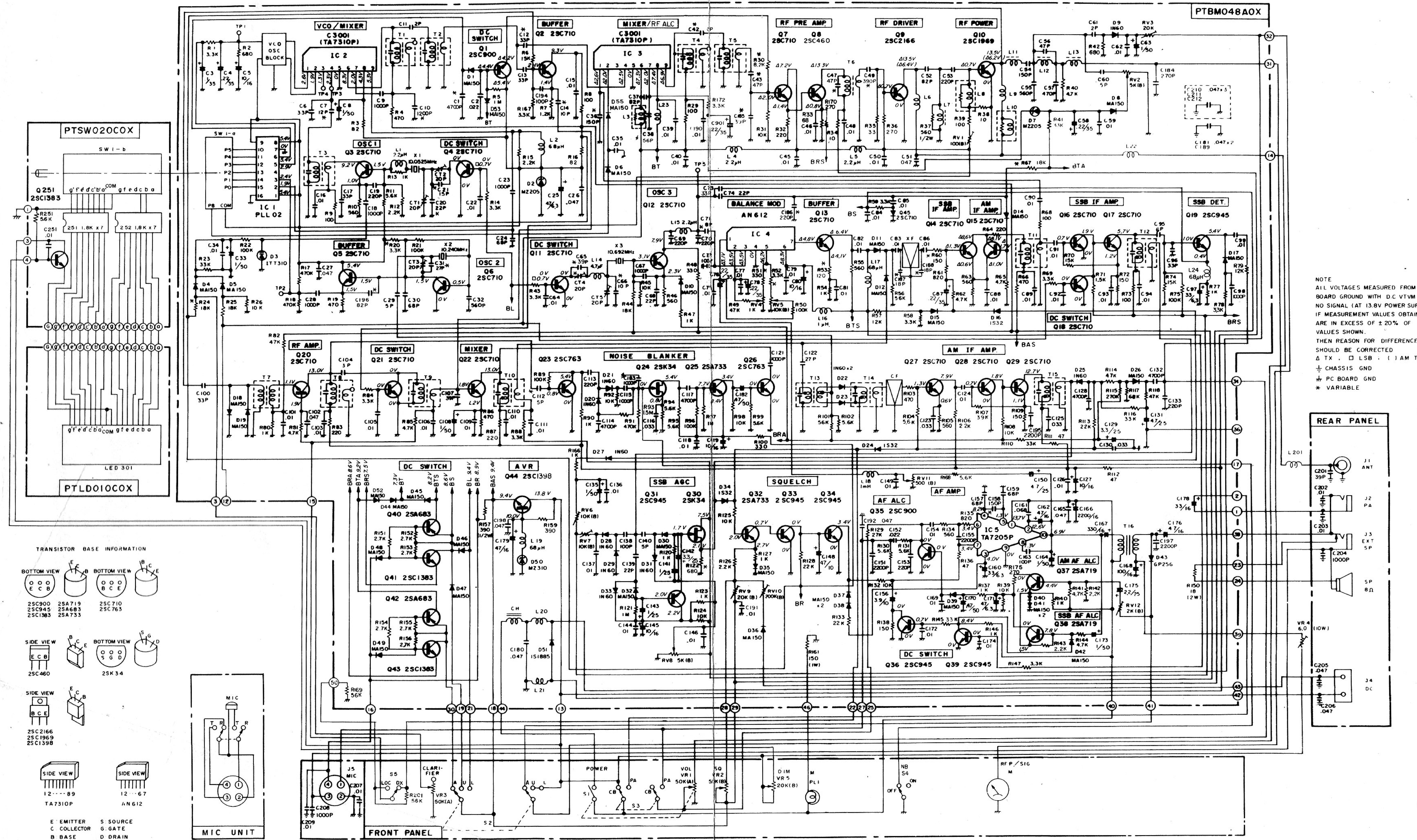
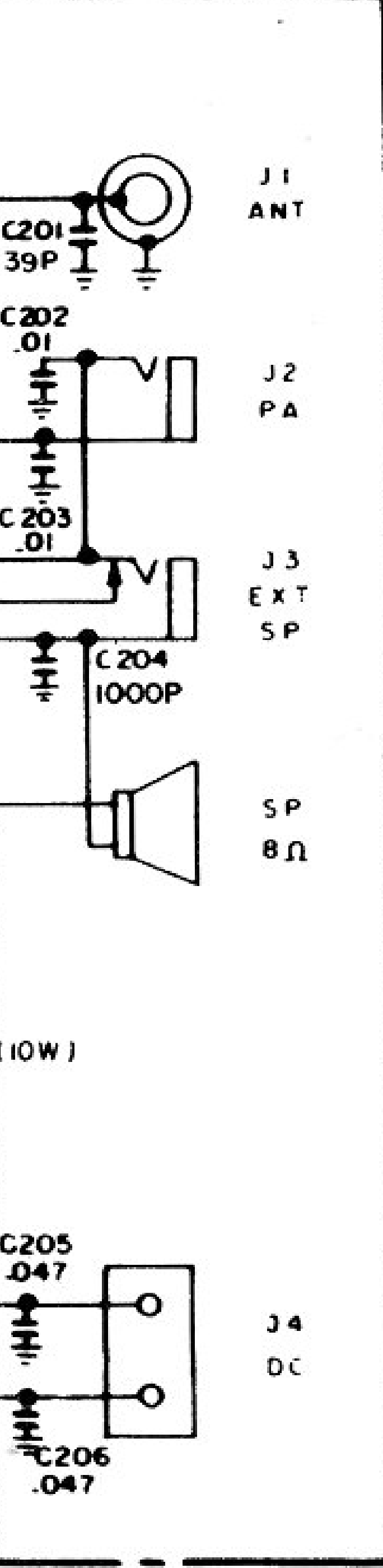


SCHEMATIC DIAGRAM MODEL SUPER PANTHER



NOTE
ALL VOLTAGES MEASURED FROM PC BOARD GROUND WITH D.C. VTM AT NO SIGNAL (AT 13.8V POWER SUPPLY). IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF $\pm 20\%$ OF VALUES SHOWN, THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED.
 Δ TX, $\square$ LSB, $\cdot$ I AM TX
 $\perp$ CHASSIS GND
 $\star$ PC BOARD GND
 $\ast$ VARIABLE

REAR PANEL



TRANSISTOR BASE INFORMATION

BOTTOM VIEW

25C900 25A719 25C710
25C945 25A683 25C763
25C1383 25A733

SIDE VIEW

25C460

BOTTOM VIEW

25K34

SIDE VIEW

25C2166 25C1969 25C1398

E EMITTER S SOURCE
C COLLECTOR G GATE
B BASE D DRAIN