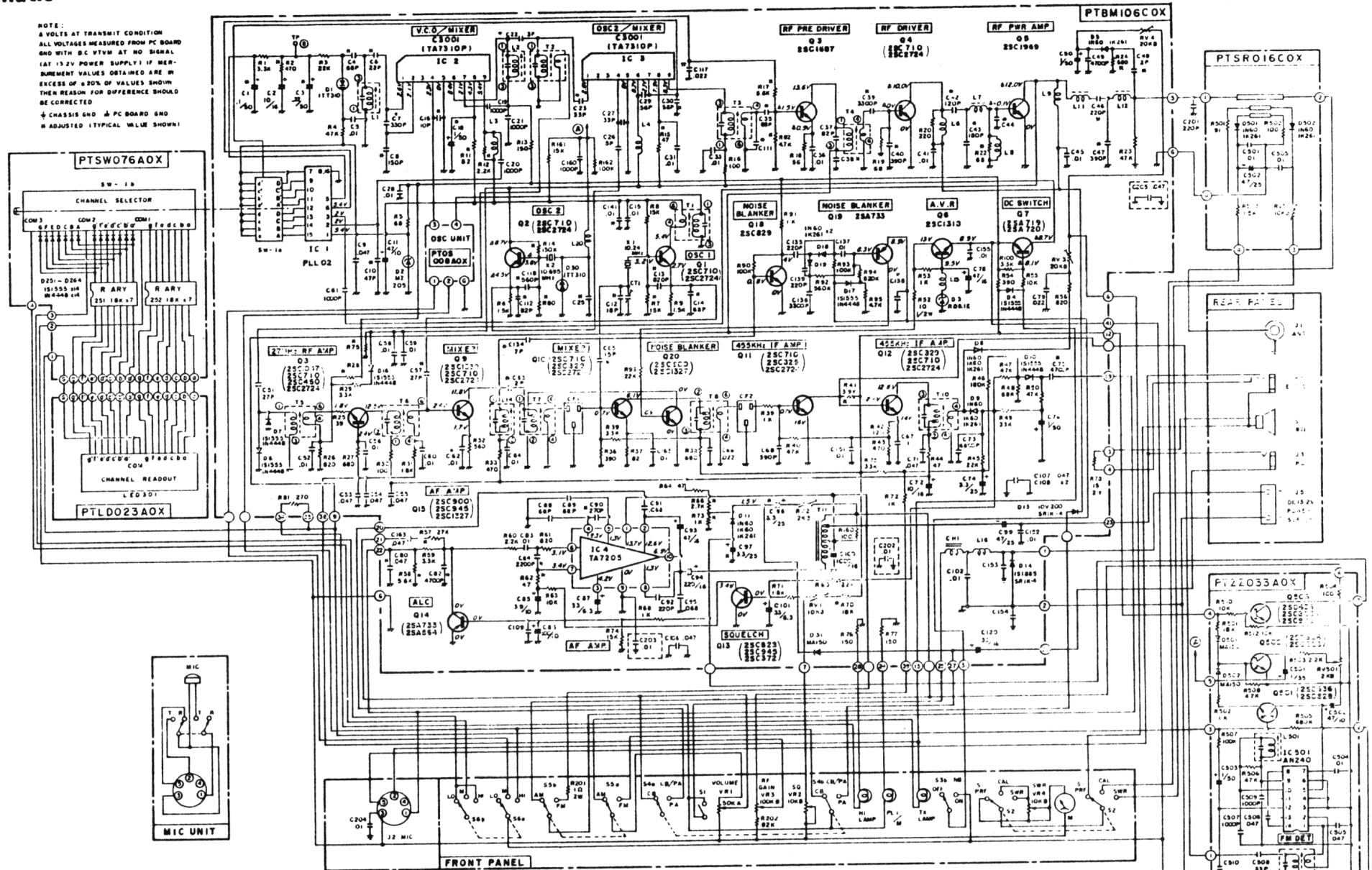


Schematic

NOTE:
 1. VOLTAGE AT TRANSMIT CONDITION
 2. ALL VOLTAGES MEASURED FROM PC BOARD
 3. AND WITH 5.0 VDC AT NO SIGNAL
 4. (AT 13.2V POWER SUPPLY) IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF 10% OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
 5. CHASSIS GND & PC BOARD GND
 6. UNLESS OTHERWISE SHOWN



TRANSFORMER TERMINATION INFORMATION (BOTTOM VIEW)



BOTTOM VIEW



BOTTOM VIEW



BOTTOM VIEW



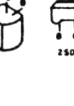
BOTTOM VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



SIDE VIEW



TRANSISTOR BASE INFORMATION

E-EMITTER C-COLLECTOR B-BASE

