# SVI 738 Xpress circuit diagrams

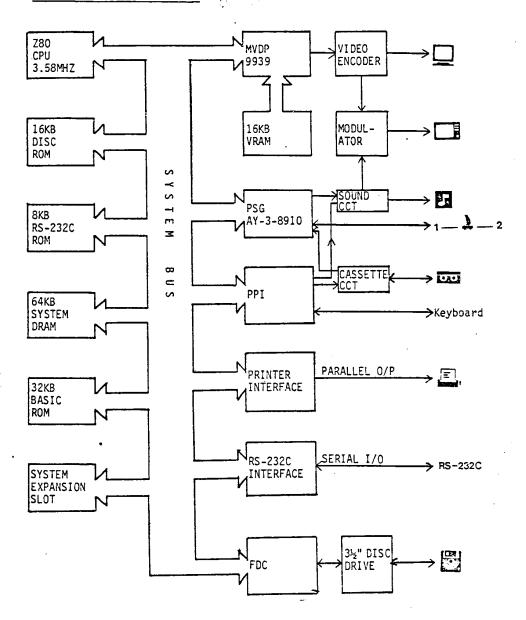
# as found in the Technical Manual

Scanned, ocr'ed and converted to PDF by HansO 2000/2001 With thanks to Alwin Henseler for stitching the pictures together!

- System architecture
- Pin assignment second drive
- Pin assignment builtin drive
- Logic board component layout
- CPU
- I/O decode and RAM refresh
- Memory decode
- PSG and cassette
- Printer interface
- Video circuit
- RS232 interface
- Keyboard matrix
- MSX ROM
- RS232 and Disk ROM
- 64KB RAM
- Floppy disk controller
- Power supply
- Joystick plug-in

# **System architecture**

#### 1.3 SVI-738 SYSTEM ARCHITECTURE



## 2.7 PIN ASSIGNMENT OF SECOND DRIVE CONNECTOR OF X'PRESS

PIN NO.	SIGNAL NAME	1/0	PIN CONNECTION
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20-25	+12V +5V +5V INDEX DRIVE SELECT I DIRECTION STEP WRITE DATA WRITE GATE TRACK O WRITE PROTECT READ DATA SIDE SELECT +12V +12V +12V +5V DRIVE SELECT O MOTOR ON READY GND*	1110 000 0 11 1 0 1110 011	CONNECTOR PIN LAYOUT: (FEMALE)  13 12 11 10 9 8 7 6 5 4 3 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

### 2.8 PIN ASSIGNMENT OF THE BUILT-IN 3.5" MICROFLOPPY DRIVE

## 2.8.1 DC POWER CONNECTOR

PIN NUMBER	SIGNAL
1	+5 V DC
2	GND
3	GND
4	+12 V DC

### 2.8.2 INTERFACE SIGNAL CONNECTOR

PIN NO.	SIGNAL	PIN NO.	SIGNAL	1/0
2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34	(RESERVED) IN USE DRIVE SELECT 3 INDEX DRIVE SELECT 0 DRIVE SELECT 1 DRIVE SELECT 1 DRIVE SELECT 2 MOTOR ON DIRECTION STEP WRITE DATA WRITE GATE TRACK 0 WRITE PROTECT READ DATA SIDE SELECT READY	1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33	GND	- I - I 0 0 - 0 0 0 0 1 I I 0 I

